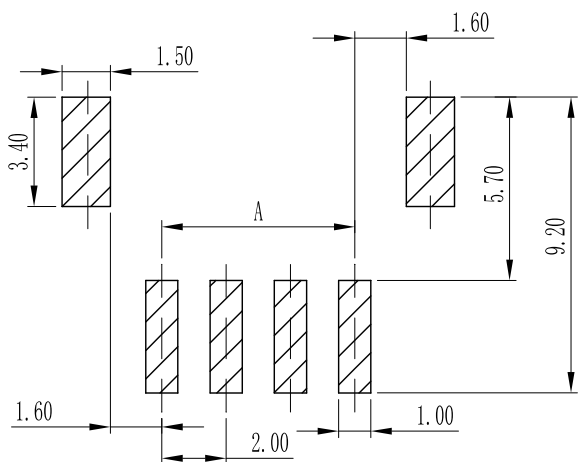
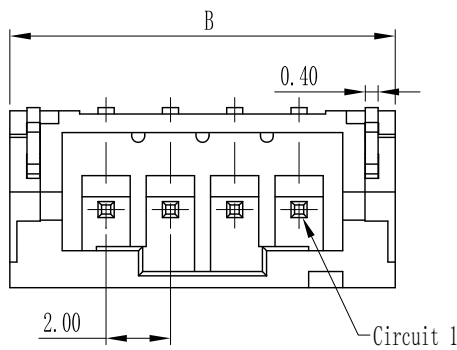
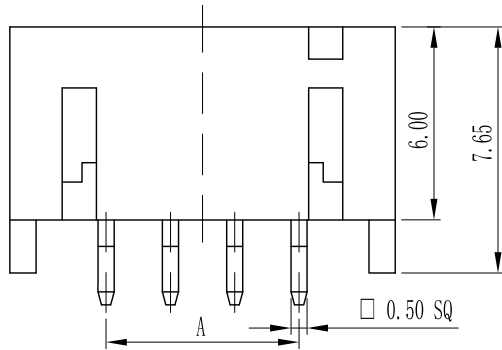
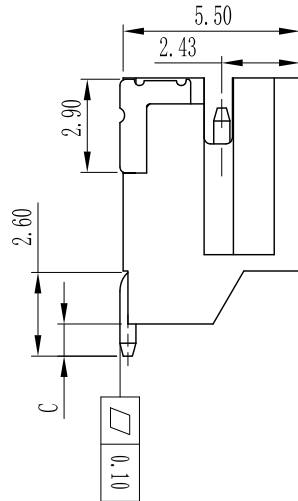
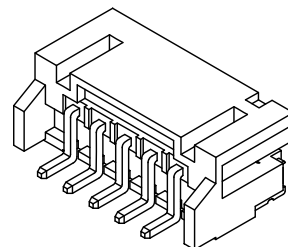




P.C.B LAYOUT
TOP VIEW
Tolerance: ± 0.05



Circuit	Dimensions	
	A	B
02P	2.00	8.00
03P	4.00	10.00
04P	6.00	12.00
05P	8.00	14.00
06P	10.00	16.00
07P	12.00	18.00
08P	14.00	20.00
09P	16.00	22.00
10P	18.00	24.00
11P	20.00	26.00
12P	22.00	28.00
13P	24.00	30.00
14P	26.00	32.00
15P	28.00	34.00
16P	30.00	36.00



Performance characteristics:

1.Main body part material

Insulator: LCP Nature UL94V-0

Contact terminal: Brass,1.27 μ m Min. Ni under plated,
outer plating Sn 3 μ m Min.

Solder:Brass,Sn

2.Electrical requirements:

Current rating: 2 A

Voltage rating: 100 V

Contact resistance: ≤ 10 m Ω

Insulation resistance: ≥ 1000 M Ω

Withstanding voltage: 800 Vr.m.s, 1mA, 60s

3.Environment specification

Working temperature: -40 $^{\circ}$ C~+105 $^{\circ}$ C

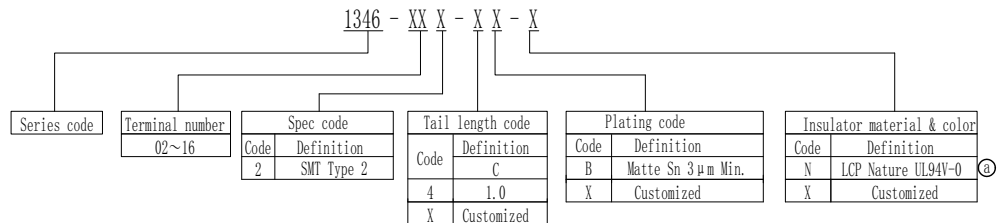
4.Packing & storing

Method of packing: Reel, outer box

Storing condition: Temperature of -10 $^{\circ}$ C~+40 $^{\circ}$ C
below 80% relative humidity

5.Style of installation: SMT

6.To prevent the pollution of the environment product info
RoHS agreement:In line with the European Union RoHS.



Unless otherwise specified tolerance:

X.	± 0.50	X. °	$\pm 3^{\circ}$
.X	± 0.38	.X°	$\pm 2^{\circ}$
.XX	± 0.25		

					2.00mm Pitch wafer SMT Type 2	SLO. 1346.004			
①	1	423968	An. Cheng	2019.10.31		Projection	Rev.	Weight	Scale
Mark	NO.	File NO.	Signature	Date			A1		
DRAW		An. Cheng 2015-06-25							
CHK		Tim 2020.01.07							
MAN									
STD		Juanli lai 2020.01.10			All Dimensions in mm		Sheet 1 of 1		
APP		Alan 2020.01.10							
					1346-XX2-4B-N	 SHINNING SHENZHEN SHINNING ELECTRONIC CO.,LTD.			